

NOTES FOR TFT:

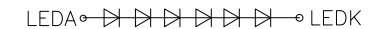
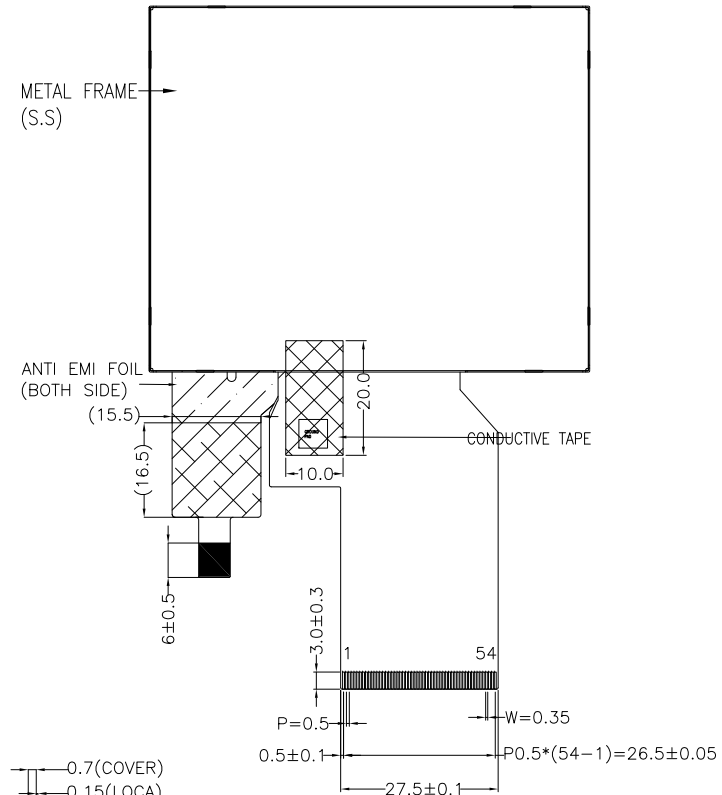
1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VDD=3.3V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: NV3052C
5. BACKLIGHT: 6-CHIP WHITE LED, If=20mA, Vf=16.8V~20.4V
6. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
7. RECOMMEND MATCHING CONNECTOR: FH19SC-54S-0.5SH OR EQUIVALENT

NOTES FOR OTHERS:

1. OPERATING TEMP.: -20°C ~ 70°C
2. STORAGE TEMP.: -30°C ~ 80°C
3. SURFACE LUMINANCE: 380 CD/M<sup>2</sup> (TYP.)(WITH CTP)
4. THE SINGLE LAYER AREA OF FPC IS BENDABLE AND THE BENDING RADIUS MUST BE NOT LESS THAN 1.5MM
5. GENERAL TOLERANCE: ±0.3MM
6. ROHS COMPLIANT

NOTES FOR CTP:

1. 7.0 INCH PROJECTIVE CAPACITIVE TOUCH PANEL
2. COVER GLASS + SENSOR GLASS + FPCA
3. OPERATING VOLTAGE: VCC=IOVCC=3.3V
4. DRIVER IC: FT5446-P03
5. INTERFACE: I<sup>2</sup>C
6. RESOLUTION: 640\*480
7. MULTI FINGER: UP TO 5
8. HARDNESS OF COVER SURFACE: >=7H
9. FIRMWARE FILE: TBD



BACKLIGHT CIRCUIT DIAGRAM

PIN ASSIGNMENT FOR LCM			
PIN	SYMBOL	PIN	SYMBOL
1	LEDK	28	RO
2	LEDK	29	R1
3	LEDA	30	R2
4	LEDA	31	R3
5	NC	32	R4
6	NC	33	R5
7	NC	34	R6
8	RESET	35	R7
9	SPENA	36	HSYNC
10	SPCLK	37	VSYNC
11	SPDAT	38	DCLK
12	B0	39	NC
13	B1	40	NC
14	B2	41	VDD
15	B3	42	VDD
16	B4	43	NC
17	B5	44	NC
18	B6	45	NC
19	B7	46	NC
20	G0	47	NC
21	G1	48	NC
22	G2	49	NC
23	G3	50	NC
24	G4	51	NC
25	G5	52	DEN
26	G6	53	GND
27	G7	54	GND

PIN ASSIGNMENT FOR CTP			
PIN	SYMBOL	PIN	SYMBOL
1	VSS	6	NC
2	VCC	7	/RST
3	SCL	8	NC
4	NC	9	/INT
5	SDA	10	VSS

**MIT** MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	20211223		MODULE P/N:	MIO350BAT-1CP
CHECKED BY:			SCALE : 1/1		
APPROVED BY:			UNIT: mm	DESCRIPTION:	TFT + CTP MODULE
VERSION NO:	01		SHEET NO.: 1/1		

01	FIRST ISSUE	RITA	2021.12.23
VER.	REVISED DESCRIPTION	REVISER	DATE